

Digital Electronics Lab



Laboratory Description :

Faculty In-charge : Dr. V. J. Dongre

Lab In-charge : Shri S. S. Pise

Area of Lab : 96.4 m²

Major Equipment/ Software :

1. DAC USING IC 0800
2. CPLD TRAINER
3. FPGA TRAINER
4. MICROCONTROLLER TRAINER
5. EPROM PROGRAMMER
6. MICROPROCESSOR TRAINER